1    What is claimed is:

2    1.   A timepiece structured to determine the elapsed time from a

3         specific event, said timepiece comprising:

4         a)   a housing including a display assembly structured to

5              display elapsed time,

6         b)   a processor including a chronographic application

7              determinative of the elapsed time,

8         c)   an activation assembly cooperatively structured with said

9              processor to selectively instigate monitoring of the

10             elapsed time from the specific event to the current time,

11             and

12        d)   said processor further comprising a restrictive

13             designation application structured to at least initially

14             limit selection of the specif event to a single

15             occurrence.

16   2.   A timepiece as recited in claim 1 wherein said display

17        assembly is structured to display current elapsed time in at

18        least one time interval.

19   3.   A timepiece as recited in claim 2 wherein said display

20        assembly is structured to display current elapsed time in a

21        plurality of different time intervals.

22   4.   A timepiece as recited in claim 2 wherein said display

23        assembly is structured to display current elapsed time

24        concurrently in a plurality of different time intervals.

25   5.   A timepiece assembly as recited in claim 4 wherein said

1    plurality of time intervals alternatively comprise two

2    different pairs of time intervals.

3    6.   A timepiece as recited in claim 1 wherein said restrictive

4         designation application is structured to restrict resetting

5         of said chronographic application.

6    7.   A timepiece as recited in claim 6 wherein said processor

7         comprises a memory capability structured to store an original

8         input time of the specific event.

9    8.   A timepiece as recited in claim 7 wherein said processor

10        further comprises a limited access application structured to

11        restrict access to said memory capability and any original

12        input time stored therein.

13   9.   A timepiece as recited in claim 8 wherein said memory

14        capability is accessed and said original input time stored

15        therein is accessed by defeat of said limited access

16        application.

17   10.  A timepiece as recited in claim 9 wherein said chronographic

18        application is responsive to said defeat of said limited

19        access application at least to the extent of being reset.

20   11.  A timepiece as recited in claim 10 wherein said chronographic

21        application is operable to determine current elapsed time

22        subsequent to the resetting thereof to a corresponding

23        original input time.

24   12.  A timepiece as recited in claim 1 wherein said processor

25        further comprises a memory capability structured to store an

1   original input time of at least one specific event.

2   13. A timepiece as recited in claim 12 wherein said memory

3   capability is further structured to store a plurality of

4   original input times, each of said plurality of original input

5   times associated with an occurrence of a different specific

6   event.

7   14. A timepiece as recited in claim 12 wherein said restrictive

8   designation application is structured to restrict resetting of

9   said chronographic application to said original input time or

10  a new input time subsequent to a predetermined adjustment time

11  period and instigation of time monitoring by said

12  chronographic application.

13  15. A timepiece as recited in claim 12 wherein said memory

14  capability is operative to automatically restore said original

15  input time to said chronographic application in the event of

16  a power failure.

17  16. A timepiece structured to determine the elapsed time from at

18  least one specific event, said timepiece comprising:

19  a)   a housing including a display assembly structured to

20  display elapsed time,

21  b)   a processor including a chronographic application

22  determinative of the elapsed time from the specific event

23  to the current time,

24  c)   said processor including a memory capability structured

25  to store at least one original input time of an

1          associated specific event,

2     d)   an activation assembly operative with said chronographic

3          application and structured to instigate continuous

4          monitoring by said chronographic application of elapsed

5          time from the specific event to the current time, and

6     e)   said processor further comprising a restrictive

7          designation application structured to initially limit

8          selection of a predetermined number of specific events

9          being monitored by said chronographic application.

10   17.  A timepiece as recited in claim 16 wherein said restrictive

11        designation application is structured to restrict resetting of

12        said chronographic application.

13   18.  A timepiece as recited in claim 17 wherein said restrictive

14        designation application is structured to restrict resetting of

15        said chronographic application by entry of an original input

16        time for a specific event other than original input times

17        associated with said predetermined numbers specific events.

18   19.  A timepiece as recited in claim 16 wherein said restrictive

19        designation application is structured to restrict entry of

20        original input time for a specific event other than original

21        input times associated with said predetermined number of

22        specific events.

23   20.  A timepiece as recited in claim 16 wherein said processor

24        further comprises a limited access application structured to

25        restrict access to said memory capability and an original

24

1            input time of an associated specific event stored therein.

2   21.   A timepiece as recited in claim 20 wherein said memory

3            capability is accessed and said original input time is

4            restored to said chronographic application by defeat of said

5            limited access application.

6   22.   A timepiece as recited in claim 16 wherein said display

7            assembly is structured to display current elapsed time

8            concurrently in a plurality of time intervals.

9   23.   A timepiece as recited in claim 16 wherein said memory

10          capability is operative to automatically restore said original

11          input time to said chronographic application in the event of

12          a power failure.

13   24.   A timepiece structured to determine the elapsed time from at

14          least one specific event, said timepiece comprising:

15          a)    a housing including a display assembly structured to

16                display elapsed time,

17          b)    a processor including a chronographic application

18                determinative of the elapsed time,

19          c)    an activation assembly operative with said chronographic

20                application and structured to instigate continuous

21                monitoring by said chronographic application of elapsed

22                time from the specific event to a current time, and

23          d)    said processor further comprising a restrictive

24                designation application structured to restrict resetting

25                of said chronographic application to any original input

1    time subsequent to instigation of elapsed time monitoring

2    by said chronographic application.

3    25.  A timepiece as recited in claim 24 wherein said processor

4    further comprises a memory capability structured to store at

5    least one original input time of an associated specific event.

6    26.  A timepiece as recited in claim 25 wherein said processor

7    further comprises a limited access application structured to

8    restrict access to said memory capability and an original

9    input time of an associated specific event stored therein.

10   27.  A timepiece as recited in claim 26 wherein said memory

11   capability is accessed and said original input time is

12   restored to said chronographic application by defeat of said

13   limited access application.